

ATTY DOCKET NO.
12656-US

SERIAL NO.
10/085,085

APPLICANT(S)
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FILING DATE
March 1, 2002

GROUP
2633

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TQL	5,809,221	September 15, 1998	Kumar et al.	395	182.02	June 7, 1995

RECEIVED
FEB 27 2003
Technology Center 2600

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
TQL	WO 00/42811	July 20, 2000	PCT				
TQL	EP 0 813 349 A2	December 17, 1997	EP				
TQL	WO 00/40040	July 6, 2000	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			"A 0.155-, 0.622-, and 2.488-Gb/s automatic bit-rate selecting clock and data recovery IC for bit-rate transparent SDH systems", J. Christoph Scheytt et al., IEEE Journal of Solid State Circuits, Vol. 34, No. 12, December 1999, pages 1935-1943.

EXAMINER

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy if this form with next communication to applicant.